

# EUROPEAN PATENT OFFICE

## Patent Abstracts of Japan

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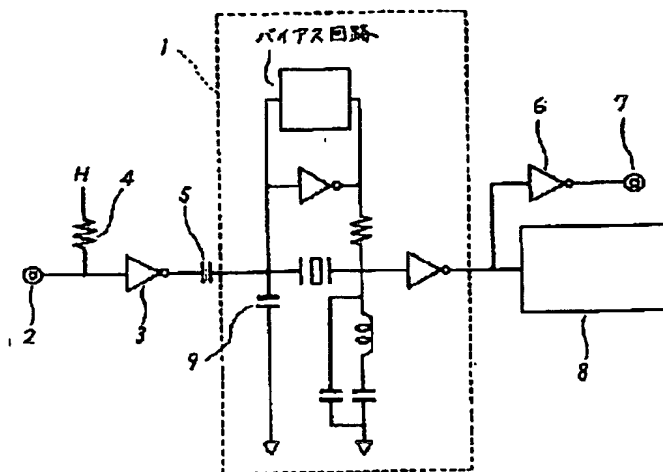
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APPLICANT : YOKOGAWA ELECTRIC CORP;

INVENTOR : SUGIHARA YOSHINOBU;

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TITLE : CLOCK SIGNAL GENERATING  
CIRCUIT



ABSTRACT : PURPOSE: To provide a clock signal generating circuit to be used for obtaining a measuring instrument capable of using rapid and synchronizing signals simultaneously with other plural measuring instruments.

CONSTITUTION: The clock signal generating circuit to be used for a measuring instrument for measuring an electric signal such as a digital oscilloscope is characteristically provided with a pull-up input terminal 2, an input buffer 3 for amplifying an input signal inputted from the input terminal 2, a crystal oscillation circuit 1 for inputting a signal from the buffer 3 through a coupling capacitor 5, an output buffer 6 for amplifying a signal outputted from the circuit 1, and an output terminal 7 for outputting a signal obtained from the buffer 6 to the external.

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